

FIG. 1

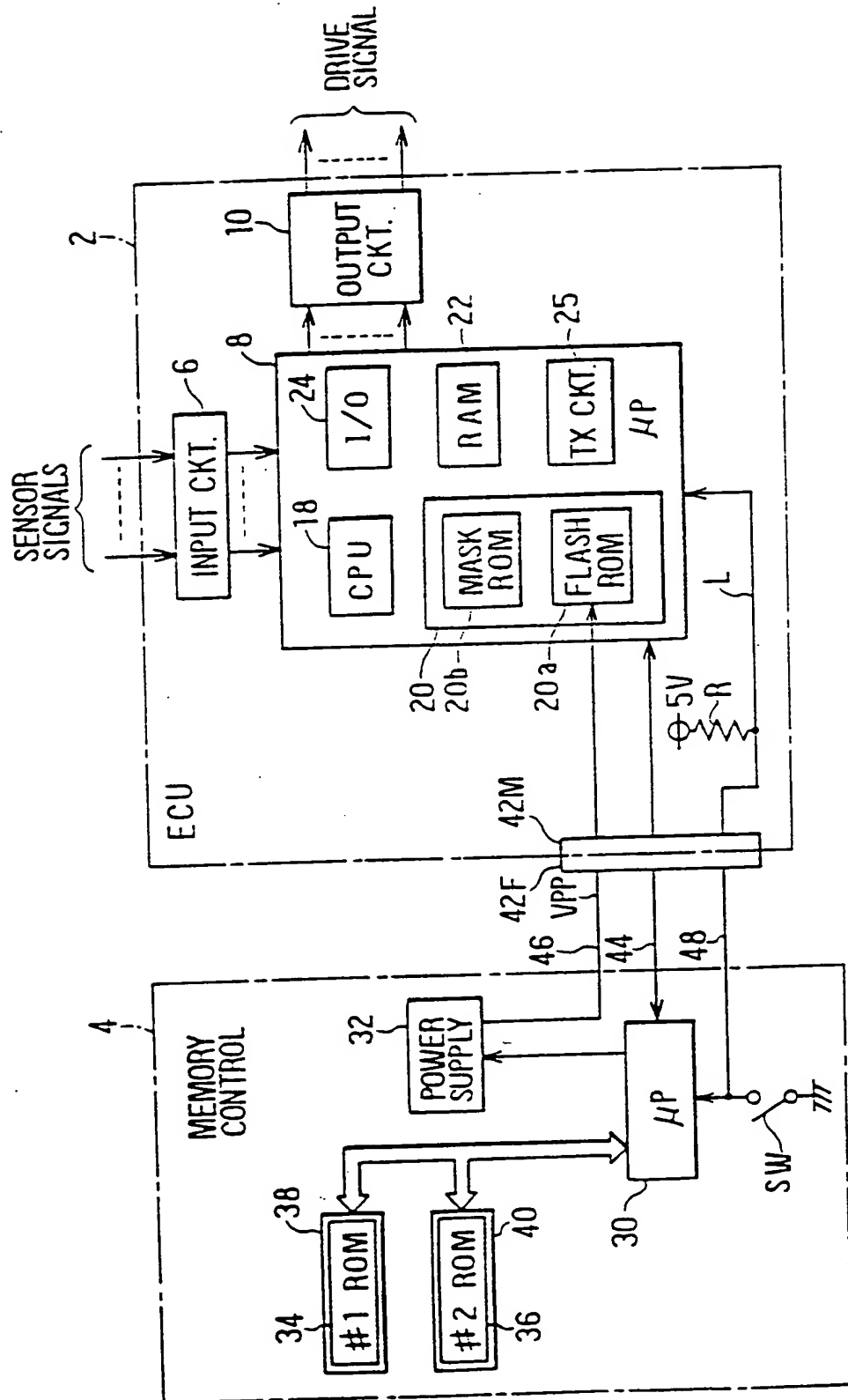




FIG. 2

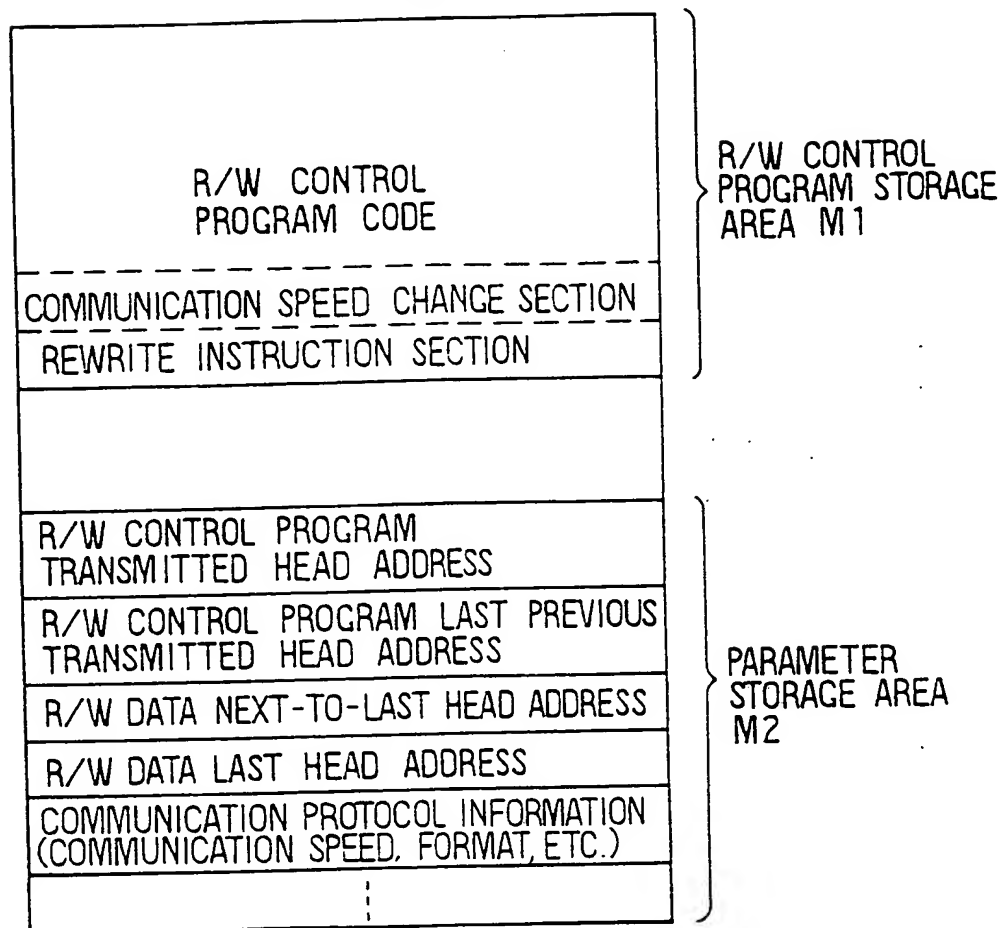


FIG. 6

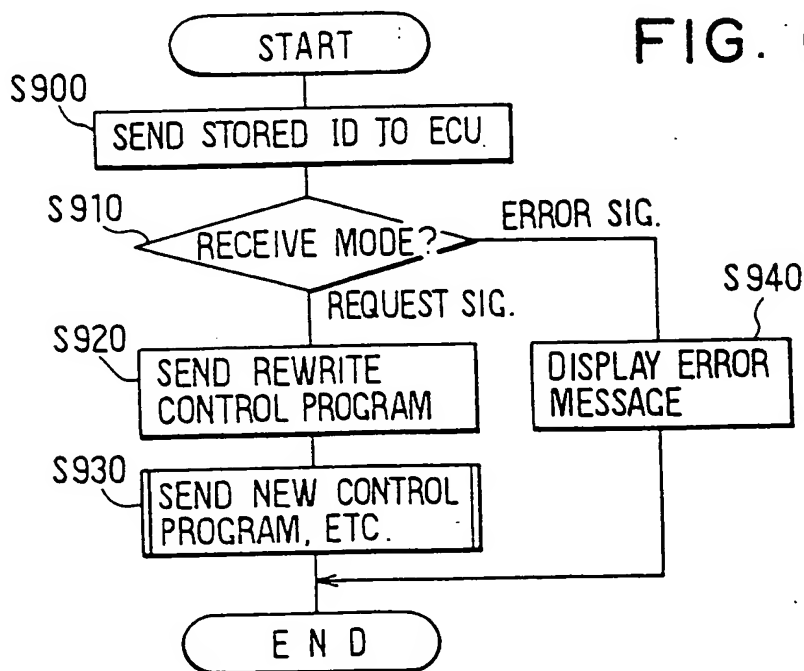




FIG. 3

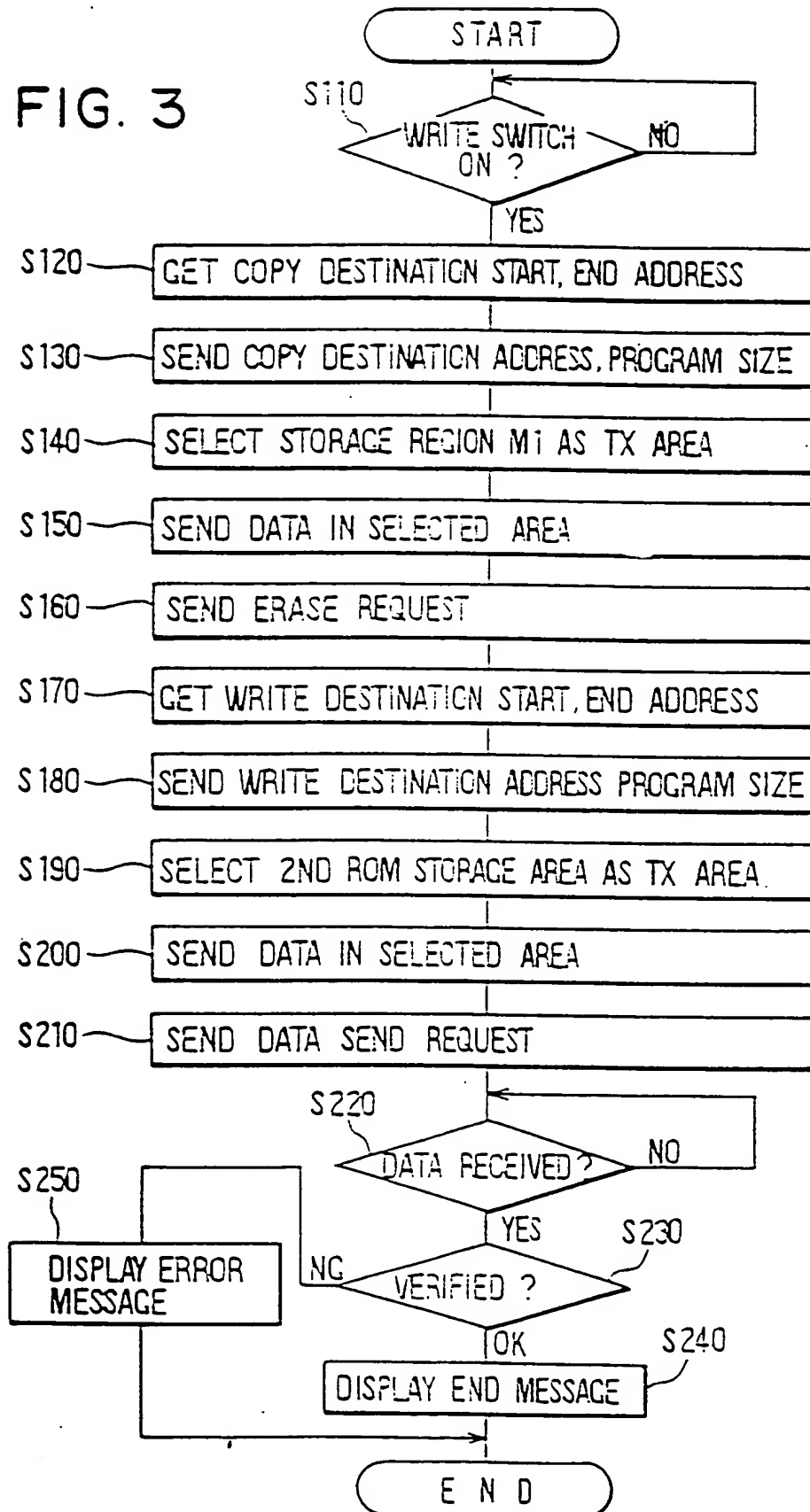




FIG. 4

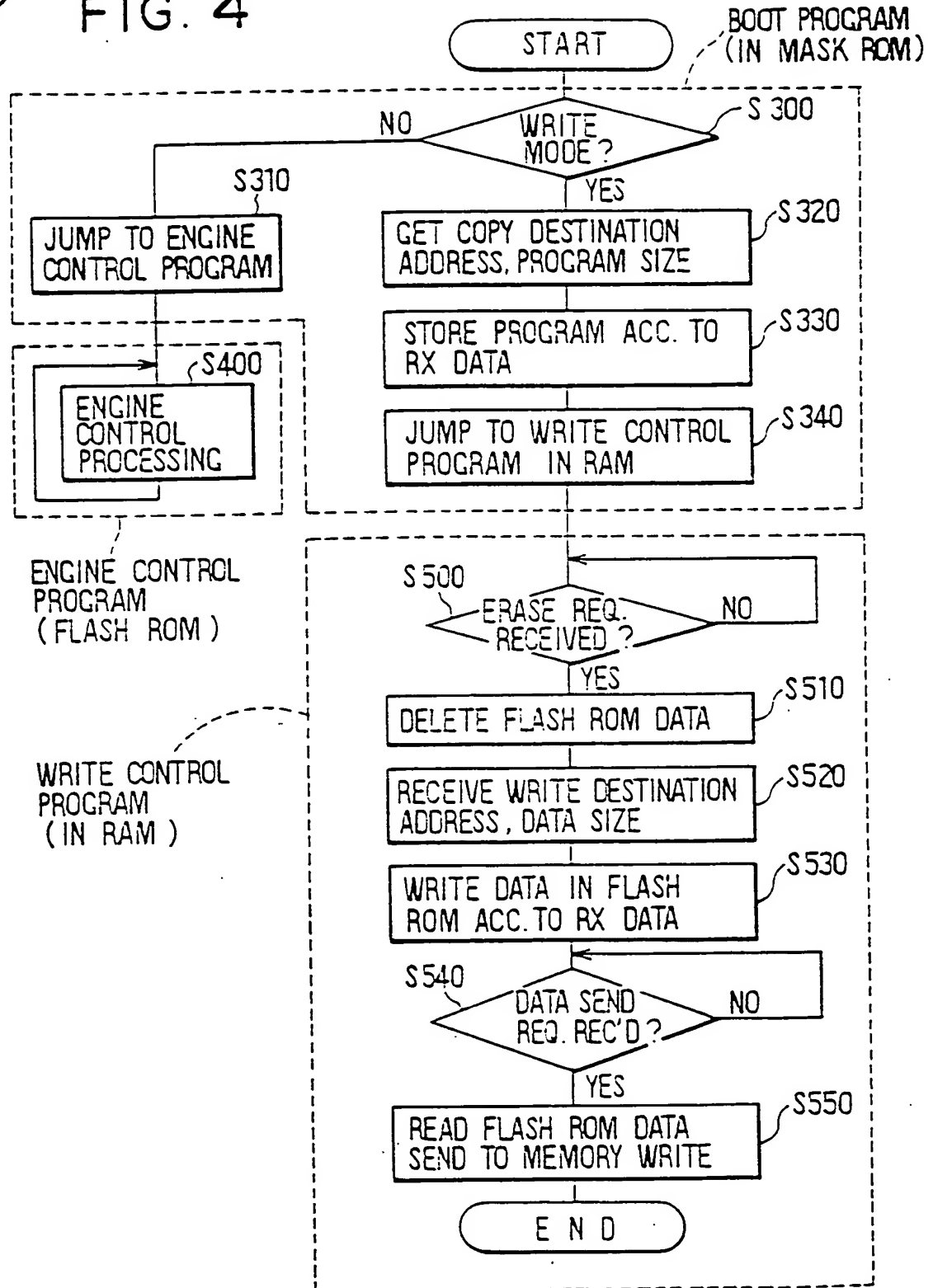




FIG. 5

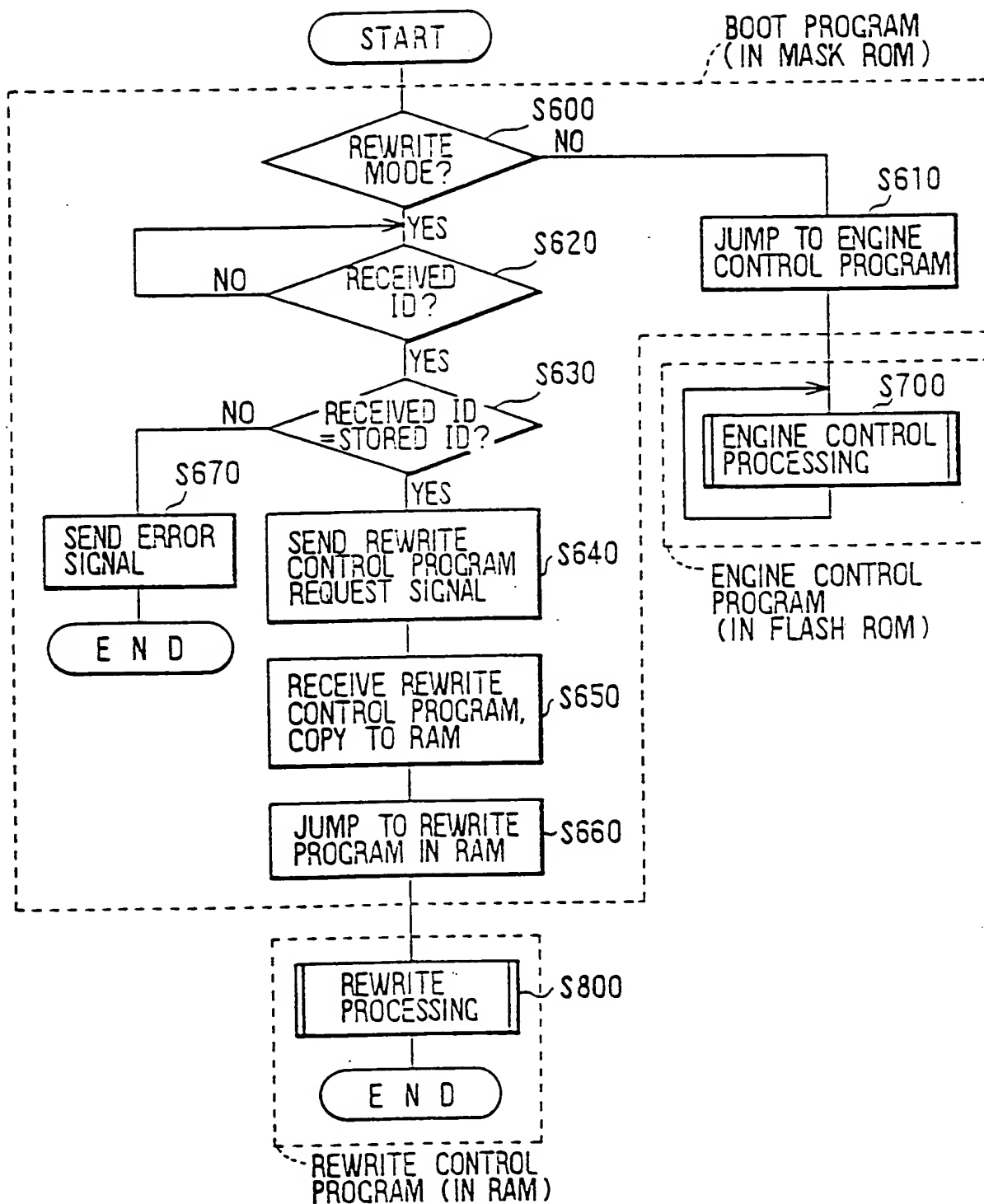


FIG. 7

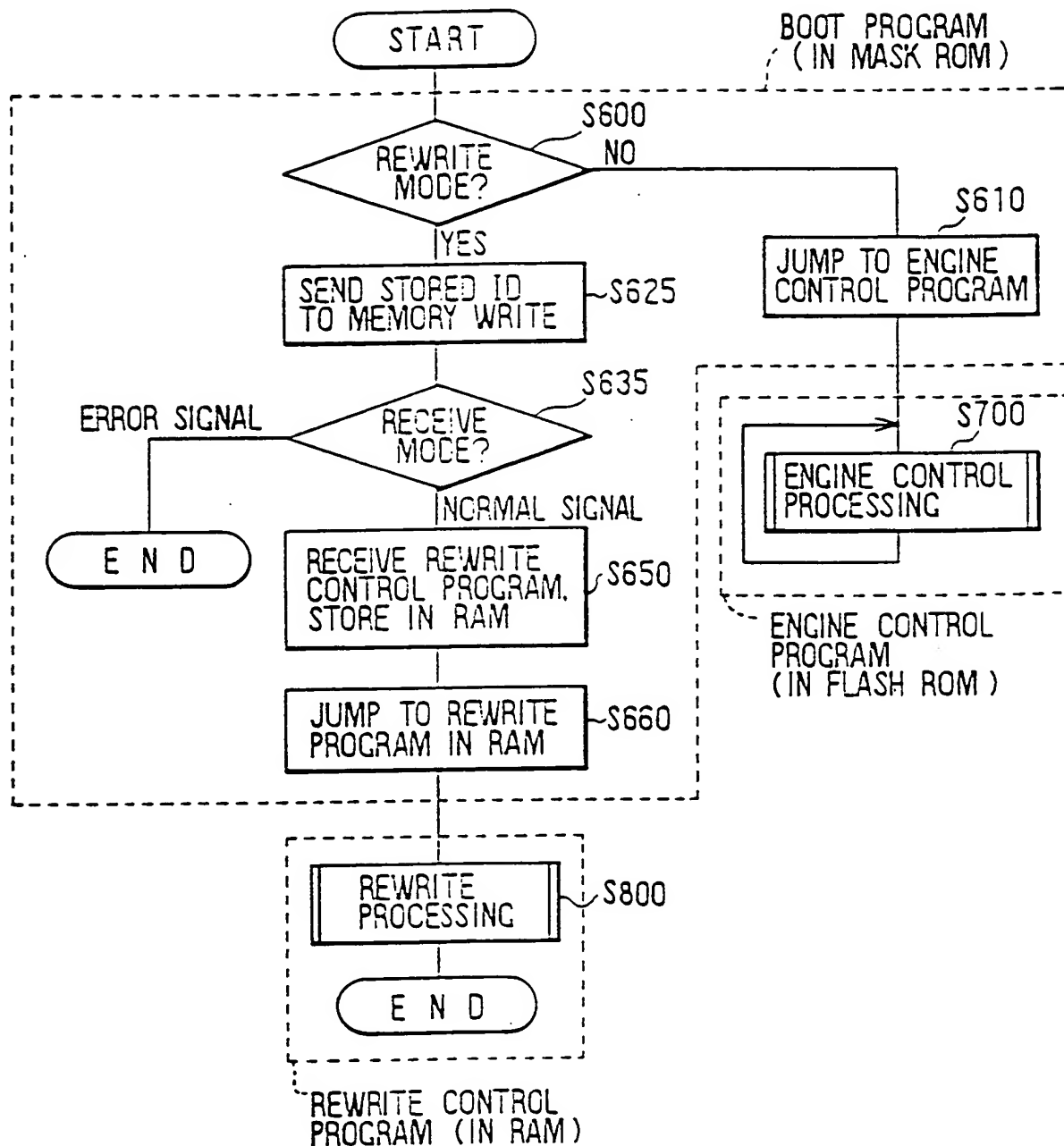




FIG. 8

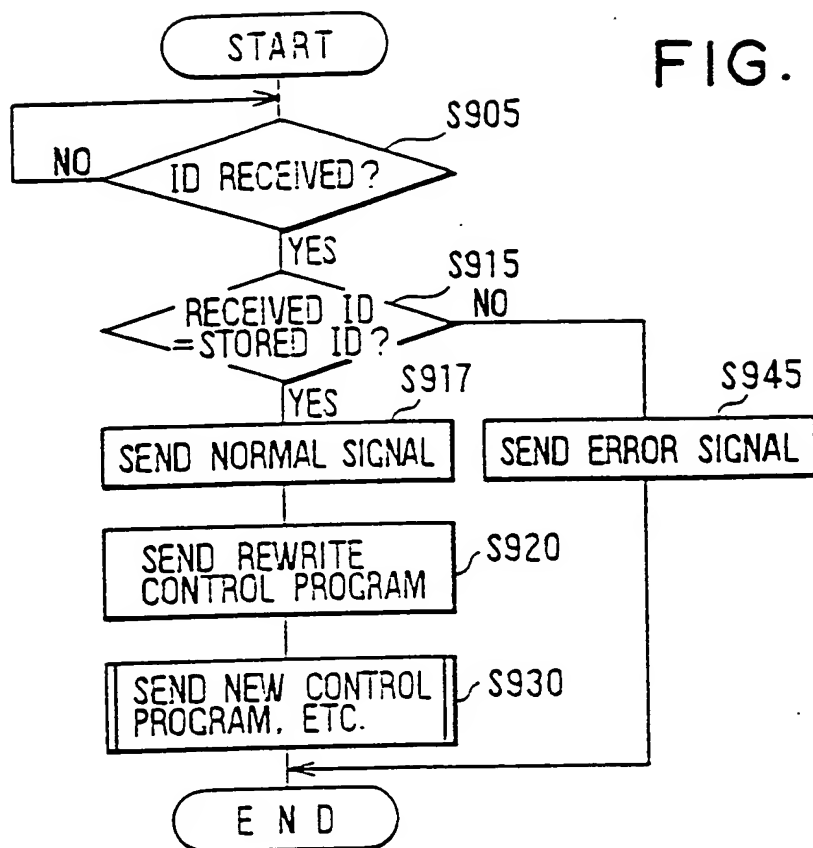


FIG. 11

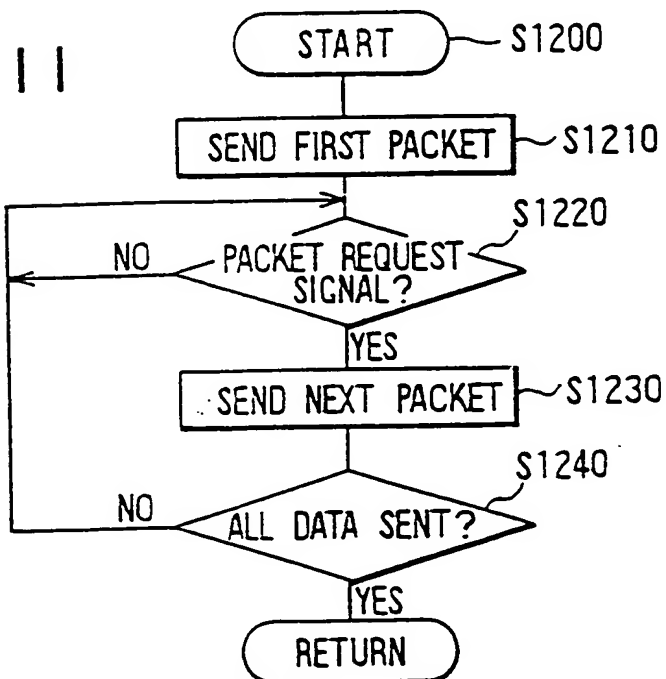




FIG. 9

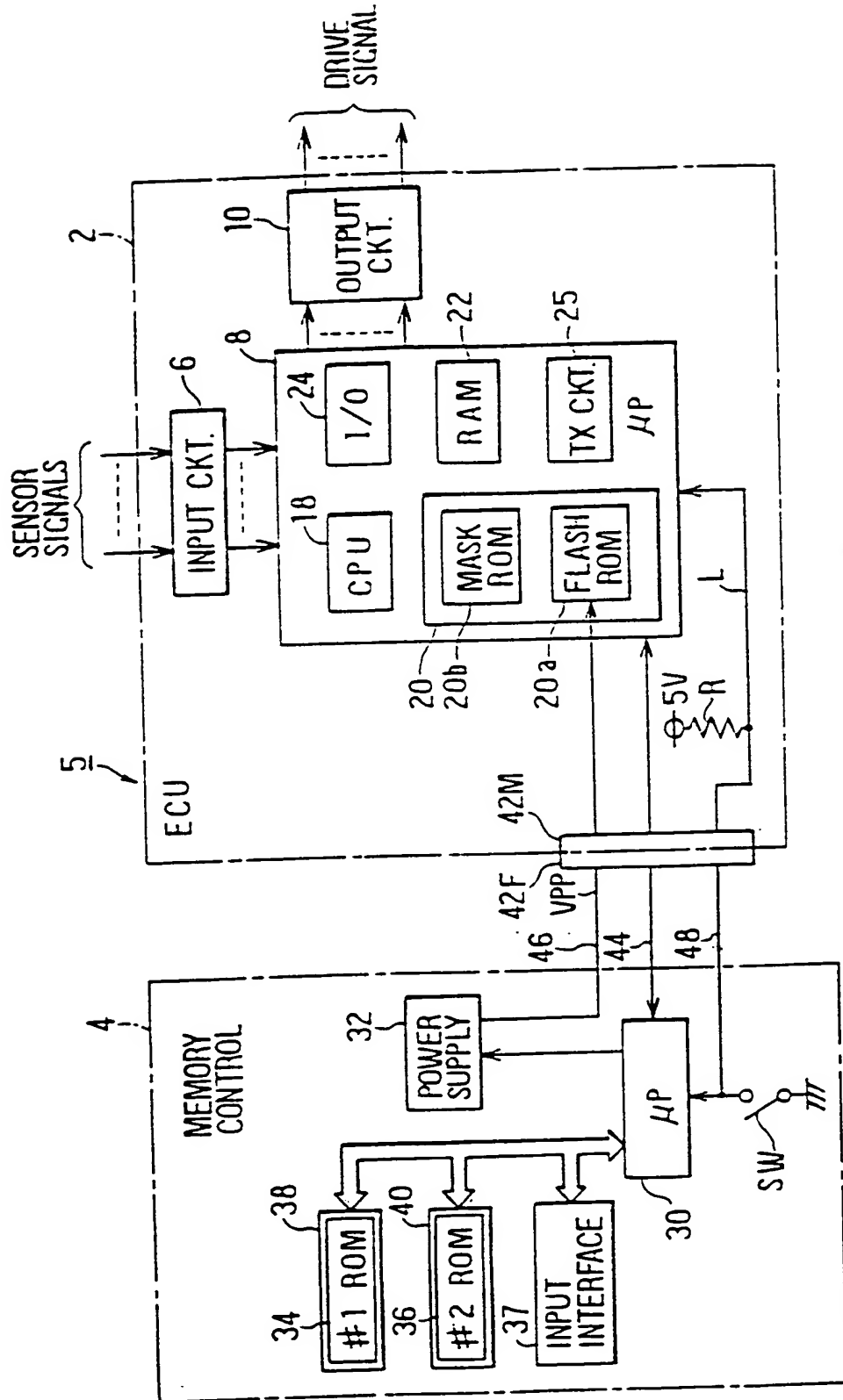




FIG. 10

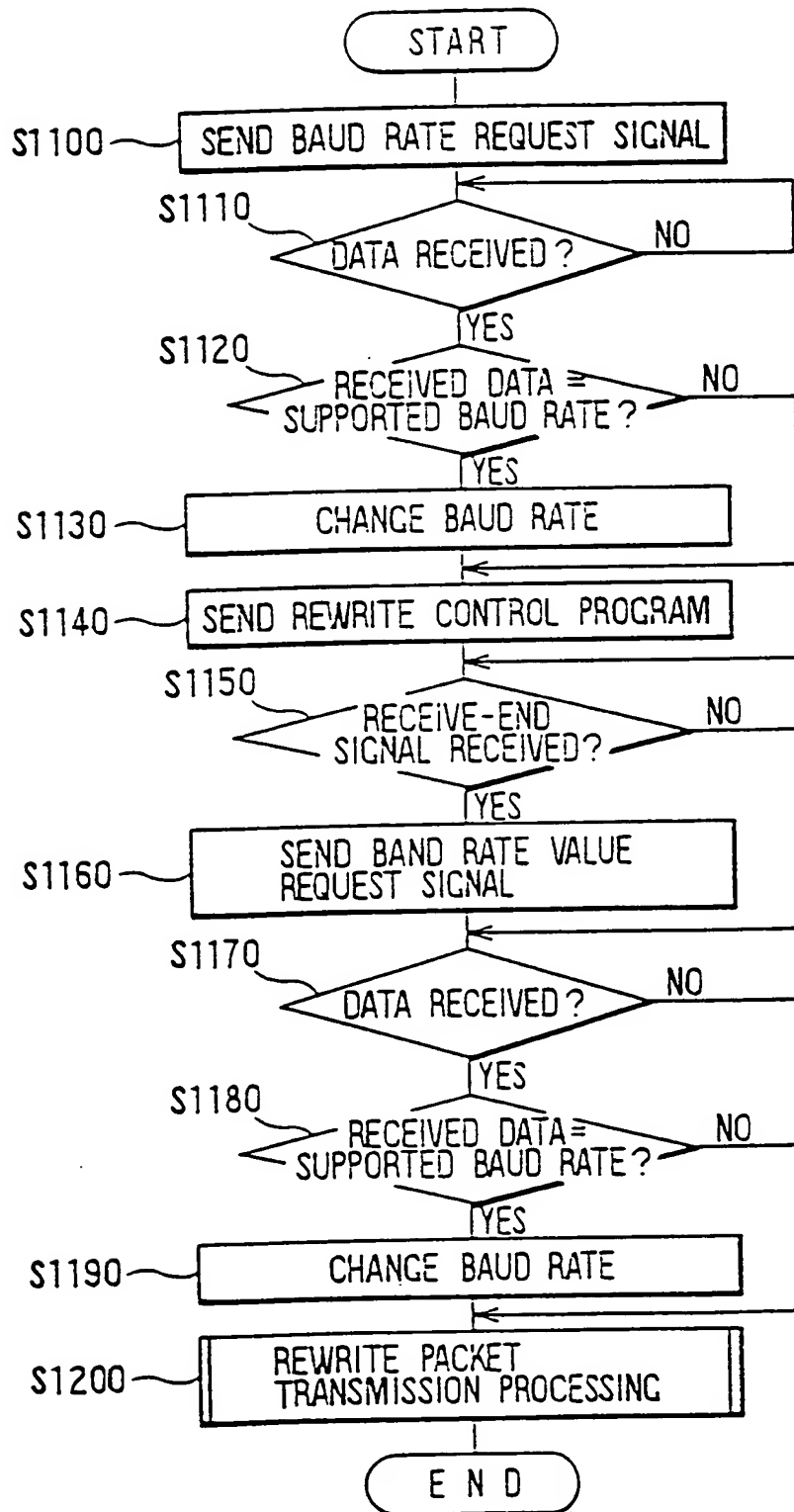




FIG. 12

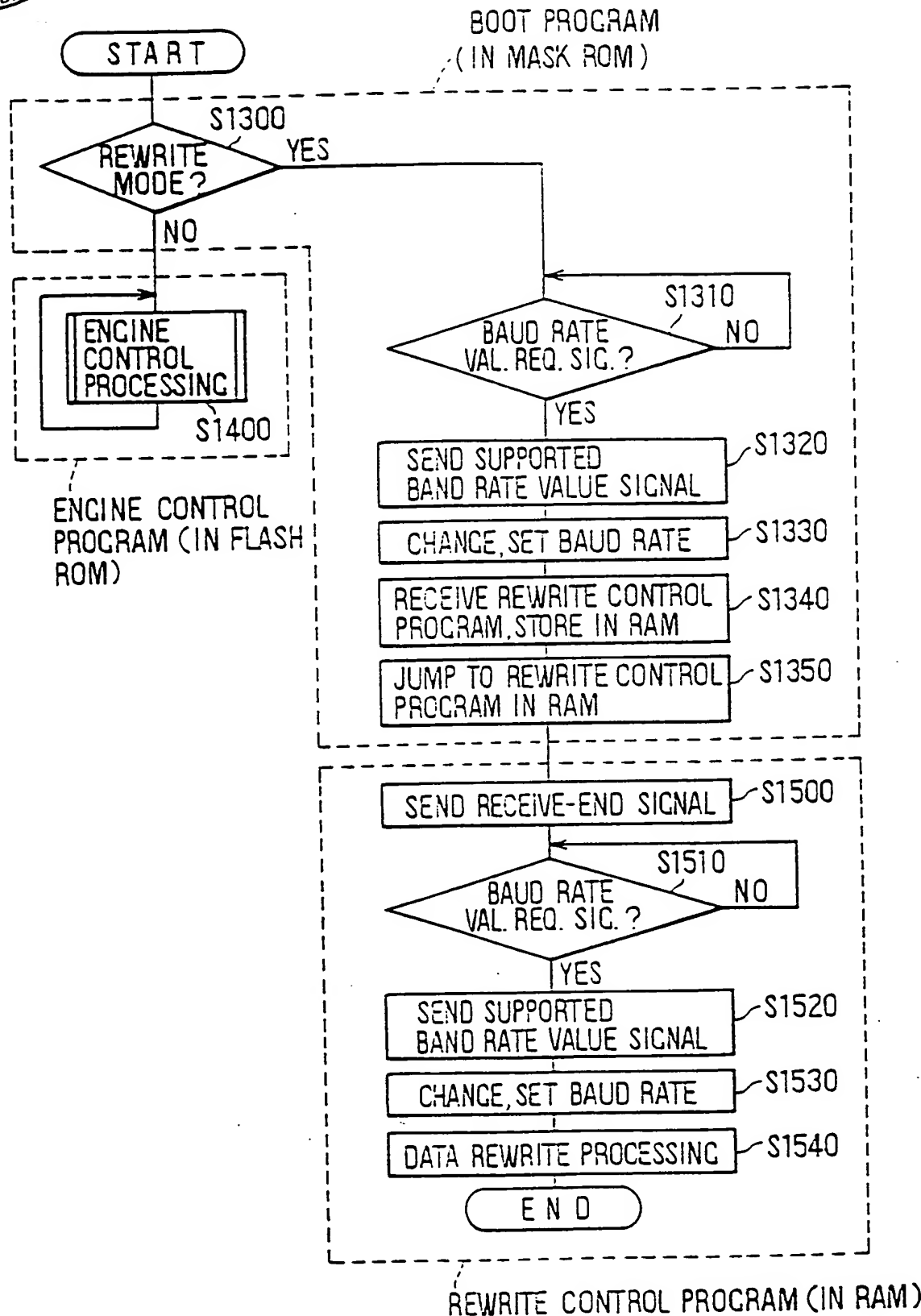


FIG. 13

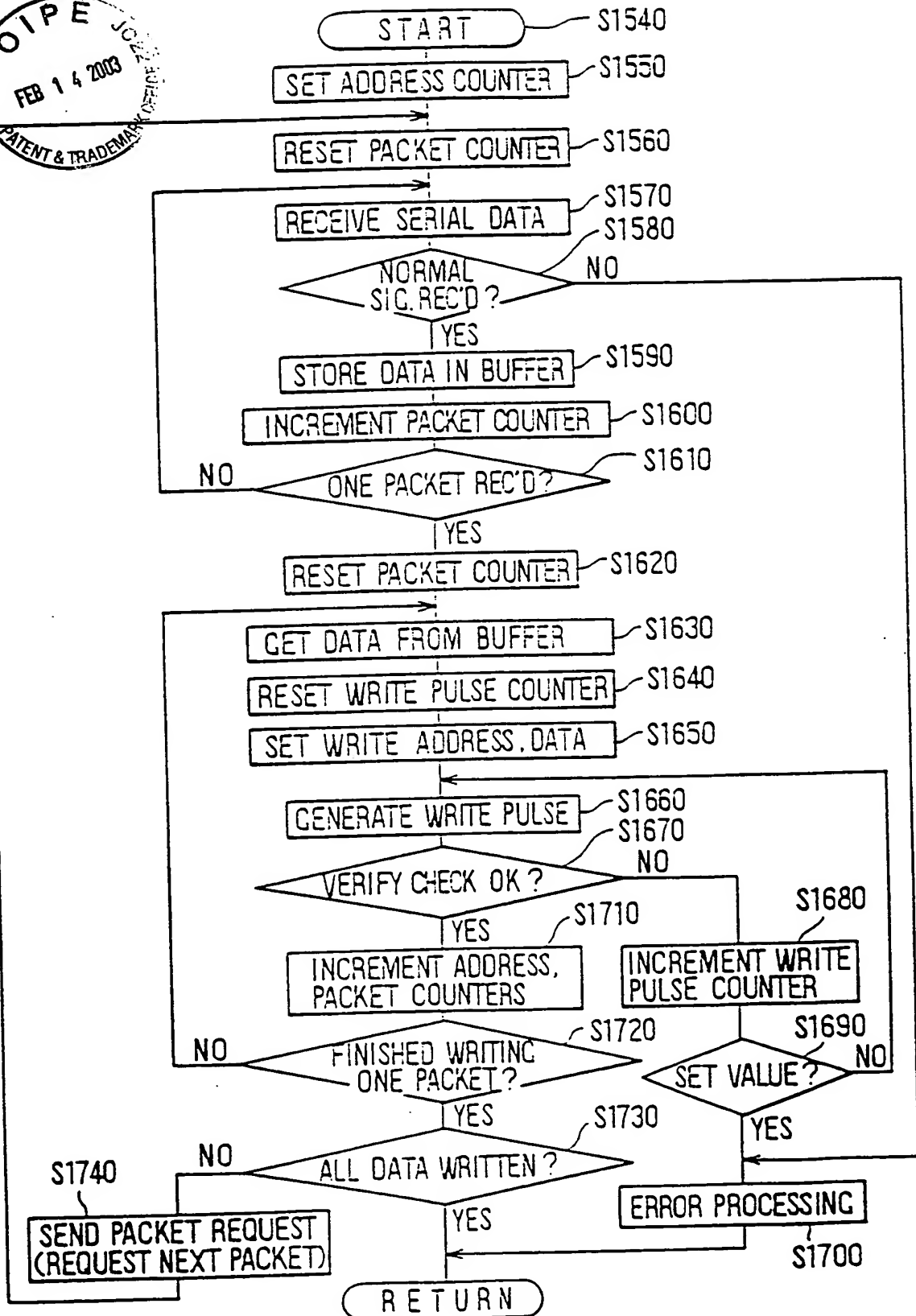
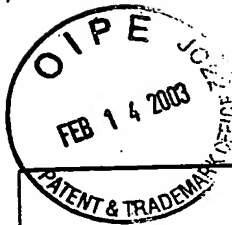




FIG. 14

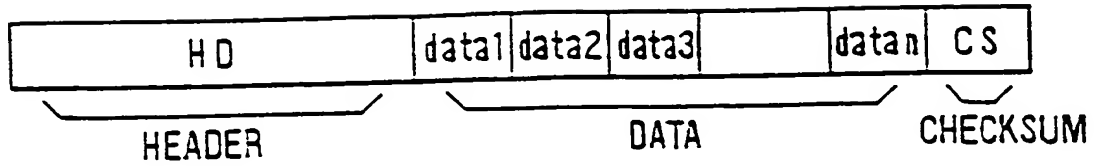


FIG. 15A

MEMORY REWRITE
 DEVICE

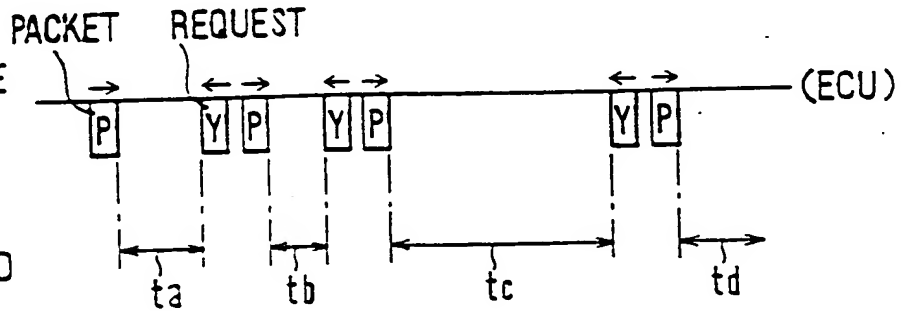


FIG. 15B

ECU SIDE DATA
 WRITING PERIOD

FIG. 16

